

Amendments to the Claims

Claims 1 and 2 (Cancelled).

3. (original) A distributed gain control circuit (DGCC) comprising:
 - an imager signal source including a shutter;
 - a timing circuit for controlling said shutter and the production of signals from said imager signal source;
 - a CDS/VGA system for receiving imager signals from said imager signal source;
 - an analog to digital converter connected to said CDS/VGA system for receiving an amplified imager signal stream from said CDS/VGA system and converting the amplified imager signal stream into digital form;
 - a digital gain circuit connected to said analog to digital converter; and
 - an automatic gain control (AGC) circuit for receiving an output digital level from said digital gain circuit for controlling the gain of said CDS/VGA system, said digital and shutter gain circuit.
4. (original) The DGCC according to claim 3 wherein said AGC circuit is coupled to said timing circuit for controlling the production of signals from said imager signal source.
5. (original) A method of gain control in an imaging system having a shutter, a digital gain circuit, and a CDS/VGA circuit, including:
 - determining total gain for an imaging system;
 - determining the level of shutter gain to be applied in the operation of the imaging system;
 - determining the level of analog gain to be applied in the operation of the imaging system; and
 - determining the level of digital gain to be applied in the operation of the imaging system.
6. (original) The method according to claim 5, wherein each gain setting for said imaging system is applied for the duration of a single frame.

7. (original) The method according to claim 5 including hierarchically adjusting shutter gain, analog (VGA) gain, and digital gain.
8. (original) The method according to claim 7 wherein the shutter gain has maximum and minimum shutter gain values.
9. (original) The method according to claim 7 wherein the analog (VGA) gain has maximum and minimum analog gain values.
10. (original) The method according to claim 7 wherein the chip gain has a maximum and a minimum gain value.
11. (original) The method according to claim 7 wherein the digital gain has a maximum and a minimum value.
12. (original) The method according to claim 8 wherein the analog (VGA) gain and the digital gain remain at a constant level as the shutter gain is varied.
13. (original) The method according to claim 8 wherein the shutter gain and the analog (VGA) gain remain at a constant level as the digital gain is varied.
14. (original) The method according to claim 8 wherein the shutter gain and the digital gain remain at a constant level as the analog (VGA) gain is varied.

Claims 15 through 34 (Cancelled).

35. (New) The method according to claim 12, wherein said constant level is user-settable.

36. (New) The method according to claim 13, wherein said constant level is user-settable.

37. (New) The method according to claim 14, wherein said constant level is user-settable.